



IFW

Docket No.: 071971-0643

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 53080
	:	
Shunsaku MURAOKA, et al.	:	Confirmation Number: 5602
	:	
Application No.: 10/584,617	:	Group Art Unit: 2824
	:	
Filed: June 26, 2006	:	Examiner: NGUYEN, NAM THANH
	:	
For: MEMORY DEVICE, MEMORY CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT HAVING VARIABLE RESISTANCE		

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits. No certification or fee is required.

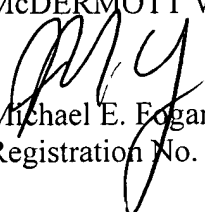
The reference was cited in a Korean Office Action and its relevance discussed therein. A copy of the Korean Office Action is attached for the Examiner's information.

10/584,617

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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**Date: April 11, 2008**

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